Output pentode primarily intended for use as line time base output valve in A.C. television receivers.

	W	<del></del>	
HEATER			
	$V_h$ $I_h$	6.3 1.4	V
	'в	•••	^
CAPACITANCES			_
	Cin	18 8.0	μμF μμF
	C <sub>out</sub> C <sub>a-g1</sub>	<1.2	μμF
	n-81		* * *
CHARACTERISTICS	$V_{\mathbf{a}}$	275	V
	$V_{g_2}^{a}$	275	v
	la <sup>®</sup>	91	mA
	\lg_2	11 -9	mA V
	$V_{gl}$ g <sub>m</sub>	-7 14	mA/V
	$\mu_{\mathrm{gl}=\mathrm{g}_2}$	16.5	
	ra	20	kΩ
OPERATION AS LINE Circuit Design	OUTPUT PENTODE		
	alve spread and for deterioration rould be designed around the follow		
	V <sub>a</sub>	90	V
	$egin{array}{c} {\sf V_{g_2}} \\ {\sf I_a} \end{array}$	275 150	V mA
For the average	e new valve the following figures		- "
3	$V_{\mathbf{a}}$	90	V
	$\bigvee_{\mathbf{g_2}}$	275 _1	V
	la	225	mÅ
Typical Circuit (Se	ee circuit on page 3)		
``	V <sub>b</sub>	300	٧
For EL38	l <sub>a</sub>	64	mĄ
	<sup>1</sup> ց <sub>2</sub> R <sub>k</sub>	18 120	mA Ω
For EBC33	l <sub>a</sub>	0.8	mÃ
N.B.—Abo	ove figures measured under synch	ronised con	ditions.
LIMITING VALUES			
	V <sub>a (b)</sub> max.	1.2	k٧
	V <sub>a</sub> max.	800	V
	$V_{a_{(pk)}}$ max. $V_{g_{2}(b)}$ max.	8 800	kV V
	$V_{g_2}$ max.	400	Ÿ
	pa max.	25	W
	p <sub>g2</sub> max.	8 200	mA
	$I_k$ max. $V_{g1}$ max. $(I_{g1} \! = \! + \! 0.3~\mu A)$	200 —1.3	m A V
	$R_{gl-k}$ max. $(p_a < 25W)$	500	kΩ
	$R_{\mathrm{gl}_{-k}}$ max. (p <sub>a</sub> $<$ 9 W)	800	kΩ
	V <sub>h-k</sub> max.	100 20	V kΩ
	$R_{h-k}$ max.	20	K 12



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CIRCUIT VALUES	(see	circuit	on	page	3	)
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	(555	circuit on page 5)		
Resistors		Value	Wattage	Tolerance
	R,	47 kΩ	↓ W	20%
	R <sub>2</sub>	330 k Ω	‡ W	10%
	$R_3^2$	50 kΩ	¼ ₩ ¼ ₩ 1 ₩	Potentiometer
	R₄	680 Ω		10%
	R <sub>5</sub>	<b>820</b> k Ω	¼ W ¼ W 1 W	20%
	$R_6$	120 Ω	ĩ W	20%
	R <sub>7</sub>	500 Ω	4 W	Potentiometer
	$R_8$	<b>2.2</b> k Ω	¼ W 4 W	20%
	R <sub>9</sub>	$2.5$ k $\Omega$		Potentiometer
	R <sub>10</sub>	<b>2.7</b> k Ω	4 W	20%
	R <sub>11</sub>	100 Ω	¼ W	20%
Capacitors		Value	Tolerance	Wkg. Voltage
	C.	0.1 μF	20%	350 V
	Č.	0.1 μF 0.0022 μF	20%	350 V
	Ċ.	0.01 μF	10%	350 V
	C,	0.001 µF	10%	350 V
	C1 C2 C3 C4	0.004–0.006 μF		500 V
	•	•		

## **Transformers**

T1 Ratio 1:3 (step-up into grid circuit)
T2 Ratio 4:1 primary inductance ≮1 H

## **Deflector Coils**

 $\begin{array}{lll} \text{Resistance} & 3 & \Omega \\ \text{Inductance} & \text{6.5 mH} \end{array}$ 

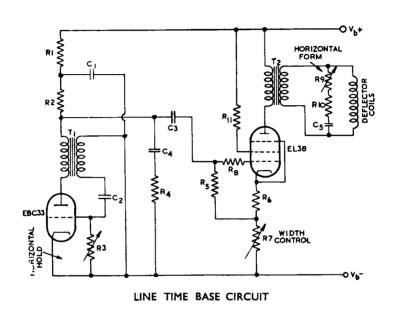
To provide full scan for 9'' picture tube  $(V_{a_2}{=}7kV)$  with peak to peak current swing of 500 mA.

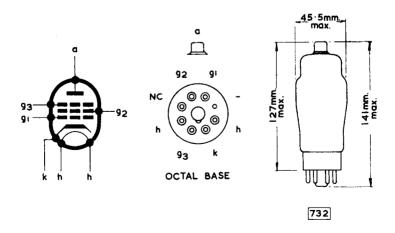
## **Notes**

- (i) Synchronising pulses may be applied negatively to the anode or positively to the grid of the EBC33.
- (ii) The decoupling components (R<sub>1</sub> C<sub>1</sub>) in the anode circuit of the EBC33 are necessary only if there is ripple on the H.T. line.
- (iii) All potentiometers should be linear components to provide smooth control.



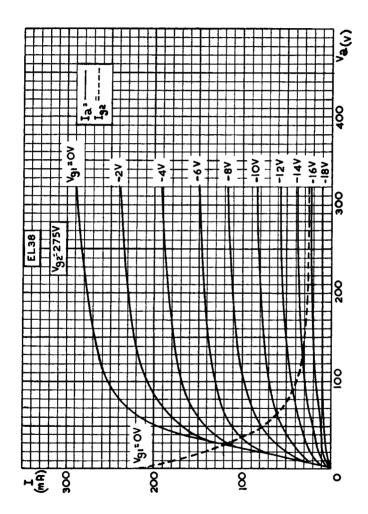
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## **EL38**

Output pentode primarily intended for use as line time base output valve in A.C. television receivers.



ANODE CURRENT AND SCREEN-GRID CURRENT PLOTTED AGAINST ANODE VOLTAGE WITH CONTROL-GRID VOLTAGE AS PARAMETER